

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicants: T.J. Gabara et al.
Case: 73-1
Serial No.: TBA
Filing Date: August 13, 1999
Group: TBA

U.S. PATENT DOCUMENTS

EXAMINER						FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS		IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER						TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS		YES NO

OTHER DOCUMENTS

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	

ph
P. Plaza et al., "A 2.5 Gb/s ATM Switch Chip Set," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 4, No. 3, pp. 405-415, September 1996.

Examiner

Pamela Rhum

Date Considered

11/2002

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.